

**S/N 10/643,585**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Steven L. Scott	Examiner: Sheng Tsai
Serial No.:	10/643,585	Group Art Unit: 2186
Filed:	August 18, 2003	Docket: 1376.700US1
Title:	LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER	

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**AMENDMENT AND RESPONSE TO NOTICE OF  
DRAWING INCONSISTENCY**

MS ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In response to the Notice of Drawing Inconsistency With Specification mailed July 9, 2007, please amend the application as follows.